

L Number	Hits	Search Text	DB	Time stamp
1	1	((access\$3 adj1 memory) near2 switch\$3) with boot\$3	USPAT	2004/09/01 09:07
11	20	(memory near2 switch\$3) with boot\$3	USPAT	2004/09/01 10:29
12	2973	x86	USPAT	2004/09/01 10:29
13	10	x86 with secur\$4	USPAT	2004/09/01 12:57
14	142	cryptographic adj1 processor\$2	USPAT	2004/09/01 12:57
15	0	(cryptographic adj1 processor\$2) same bridge same memory	USPAT	2004/09/01 12:58
16	13	(cryptographic adj1 processor\$2) same switch\$4	USPAT	2004/09/01 13:01
17	67	(cryptographic adj1 processor\$2) same memory	USPAT	2004/09/01 13:02
18	15	((cryptographic adj1 processor\$2) same memory) and bridge\$2	USPAT	2004/09/01 13:02
19	5	((cryptographic adj1 processor\$2) same memory) and bridge\$2 and boot\$3	USPAT	2004/09/01 13:14
20	0	((cryptographic adj1 processor\$2) same memory) and (boot\$3 adj1 switch)	USPAT	2004/09/01 13:15
21	0	((cryptographic adj1 processor\$2) same memory) and (boot\$3 near1 switch)	USPAT	2004/09/01 13:15
22	0	(cryptographic adj1 processor\$2) and (boot\$3 adj1 switch)	USPAT	2004/09/01 13:15
23	0	(cryptographic adj1 processor\$2) and (boot\$3 near1 switch)	USPAT	2004/09/01 13:15
24	0	(cryptographic adj1 processor\$2) and (boot\$3 with switch)	USPAT	2004/09/01 13:25
25	61	(boot\$3 adj1 switch)	USPAT	2004/09/01 13:15
26	6	((boot\$3 adj1 switch)) and encrypt\$	USPAT	2004/09/01 13:15
27	15	(cryptographic adj1 processor\$2) and (boot\$3)	USPAT	2004/09/01 13:25
28	5	((cryptographic adj1 processor\$2) and (boot\$3)) and bridge\$2	USPAT	2004/09/01 13:39
29	3	(secur\$ adj1 boot\$3) and encrypt\$ and bridge\$2	USPAT	2004/09/01 13:40
-	0	processor and bridge\$2 and switch\$3 and memory and (cryptographic with processor\$2)	USPAT; US-PGPUB; DERWENT	2004/08/31 12:09
-	0	processor and bridge\$2 and switch\$3 and (cryptographic with processor\$2)	USPAT; US-PGPUB; DERWENT	2004/08/31 12:09

-	17241	processor and bridge\$2 and switch\$3 and memory	USPAT; US-PGPUB; DERWENT	2004/08/31 12:10
-	404	(processor and bridge\$2 and switch\$3 and memory) and (crypto or cryptographic)	USPAT; US-PGPUB; DERWENT	2004/08/31 12:10
-	482	processor same bridge\$2 same switch\$3 same memory	USPAT; US-PGPUB; DERWENT	2004/08/31 12:10
-	10	(processor same bridge\$2 same switch\$3 same memory) and (crypto or cryptographic)	USPAT; US-PGPUB; DERWENT	2004/08/31 12:27
-	2	6385705.pn.	USPAT; US-PGPUB; DERWENT	2004/08/31 12:28
-	2	6195749.pn.	USPAT; US-PGPUB; DERWENT	2004/08/31 12:44
-	140	(processor same bridge\$2 same switch\$3 same memory) and (switch\$ with bridge\$2 with memory)	USPAT; US-PGPUB; DERWENT	2004/08/31 12:44
-	19	(processor same bridge\$2 same switch\$3 same memory) and (switch\$ with bridge\$2 with memory with between)	USPAT; US-PGPUB; DERWENT	2004/08/31 13:06
-	24897	switch\$3 adj1 mechanism	USPAT; US-PGPUB; DERWENT	2004/08/31 13:06
-	0	(switch\$3 adj1 mechanism) with bridge with memory with processor	USPAT; US-PGPUB; DERWENT	2004/08/31 13:07
-	5	(switch\$3 adj1 mechanism) same bridge same memory same processor	USPAT; US-PGPUB; DERWENT	2004/08/31 13:27
-	12	switch\$ same bridge same memory same processor same pci same agp	USPAT; US-PGPUB; DERWENT	2004/08/31 15:57
-	411	security adj1 switch	USPAT; US-PGPUB; DERWENT	2004/08/31 15:58
-	0	(security adj1 switch) same processor same memory same boot\$	USPAT; US-PGPUB; DERWENT	2004/08/31 15:58
-	11	(security adj1 switch) same boots	USPAT; US-PGPUB; DERWENT	2004/08/31 16:13
-	0	(memory near1 access) with (boot near1 only)	USPAT; US-PGPUB; DERWENT	2004/08/31 16:13

-	1	(memory near1 access\$3) with (boot\$3 near1 only)	USPAT; US-PGPUB; DERWENT USPAT	2004/08/31 16:13
-	10	("5422883" "5450411" "5621898" "5640392" "5742847" "5758105" "5761430" "5761448" "5872998" "5948080").PN.		2004/09/01 08:23